



PATENT

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May 10, 2004
Date

Denise Sheridan
Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Joseph M. Jeddeloh	Attorney Docket No.:	501318.01
Serial No.	: 10/747,984	Group Art Unit	: 2186
Filed	: December 29, 2003	Examiner	: Not yet assigned
Title	: MEMORY HUB AND METHOD FOR MEMORY SYSTEM PERFORMANCE MONITORING		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. A copy of the cited non-patent literature reference, as required under 37 C.F.R. § 1.98(a)(2), is enclosed. Copies of the cited U.S. patents and U.S. patent application publications will not be submitted herewith in accordance with the waiver by the Office of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for U.S. national patent applications filed after June 30, 2003. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,
DORSEY & WHITNEY LLP

A handwritten signature in black ink, reading "Edward W. Bulchis". The signature is fluid and cursive, with the first letters of the first and last names being capitalized and prominent.

Edward W. Bulchis
Registration No. 26,847

EWB:dms

Enclosures:

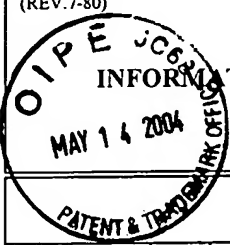
Postcard

Form PTO-1449

Cited Reference (1)

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FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 501318.01		APPLICATION NO. 10/747,984	
 <p>INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)</p>				APPLICANT(S) Joseph M. Jeddelloh			
				FILING DATE December 29, 2003		GROUP ART UNIT 2186	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
AA	5,818,844	10/06/98	Singh et al.	370	463		
AB	6,272,609	08/07/01	Jeddelloh	711	169		
AC	6,477,592	11/05/02	Chen et al.	710	52		
AD	6,462,978	10/08/02	Shibata et al.	365	63		
AE	6,523,092	02/18/03	Fanning	711	134		
AF	6,523,093	02/18/03	Bogin et al.	711	137		
AG	6,622,227	09/16/03	Zumkehr et al.	711	167		
AH	6,631,440	10/07/03	Jenne et al.	711	105		
AI	2002/0144064	10/03/02	Fanning	711	144		
AJ	2003/0005223	01/02/03	Coulson et al.	711	118		
AK	2003/0229770	12/11/03	Jeddelloh	711	213		
AL							
AM							
AN							
AO							
AP							
AQ							
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
AR							
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
AS	Intel, "Intel 840 Chipset: 82840 Memory Controller Hub (MCH)", Datasheet, October 1999, pp. 1-178.						
EXAMINER				DATE CONSIDERED			
<p>* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).</p>							